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viva questions: 1. implement the following function using vhdl coding. (try to minimize if you can).

$f(a,b,c,d)=(a^2+b+c) \cdot (a+b^2+d^2) \cdot (b+c^2+d^2)$.

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objective of the experiment. to study about the simulation tools available in xilinx project navigator using verilog tools. facilities required and ...

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Logic Design Laboratory Manualvtu Elearning Centre

experiment: 1. logic gates. aim: to study and verify the truth table of logic gates. learning objective: identify various ics and their specification.

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